DOCKET NO. 99-B-186 (STMI01-99186)   PATENT
Customer No. 30425

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	:	Vidyabhusan Gupta
Serial No.	:	09/591,621
Filed	:	June 9, 2000
For	:	SYSTEM AND METHOD FOR DESIGNING AND OPTIMIZING THE MEMORY OF AN EMBEDDED PROCESSING SYSTEM
Group No.	:	2128
Examiner	:	Herng-der Day

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:
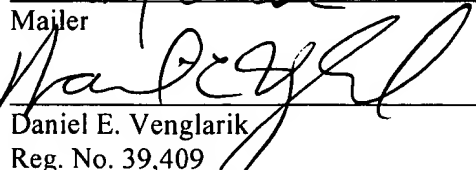
1.   Letter to Official Draftsman;
2.   Two (2) sheets of "replacement" formal drawings; and
3.   Postcard receipt

relating to the above application, were deposited as "First Class Mail", with the United States Postal Service, addressed to: MAIL STOP ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 24 , 2008.

Date: 4/24/08

Date: 4-24-2008

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Attn: Official Draftsman

Sir:

LETTER TO OFFICIAL DRAFTSMAN

The Applicant submits two (2) sheets of formal drawings (FIGURES 1-3) in the above-referenced

patent application.

Respectfully submitted,

MUNCK BUTRUS CARTER, P.C.

Date: 4-24-2008

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